

# Quantum Error Correction.

Logical bit  $\Rightarrow$  Many physical  
bits.

$1 \Rightarrow 111$   $0 \Rightarrow 000$  Majority rules  
Measure bits, find out which value  
is in majority + flip back the  
minority bit.

5 bits 11111. Measurement destroys  
superposition. in Q.Case.

$$|\psi\rangle \Rightarrow |\psi\rangle|\psi\rangle|\psi\rangle$$

$$|\psi\rangle \Rightarrow |\psi\rangle|\psi\rangle$$

$$\alpha|11\rangle + \beta|00\rangle \Rightarrow \alpha^2|11\rangle|11\rangle + \alpha\beta(|11\rangle|00\rangle + |00\rangle|11\rangle) + \beta^2|00\rangle|00\rangle$$

Q.M is linear. (Unitary of S.)

$$U(|\psi\rangle + |\phi\rangle) = U|\psi\rangle + U|\phi\rangle$$

No cloning Thm.

$$\left. \begin{array}{l} |1\rangle|0\rangle \Rightarrow |11\rangle|11\rangle \\ |0\rangle|0\rangle \Rightarrow |00\rangle|00\rangle \end{array} \right\} \text{Unitary.}$$

C NOT  $\rightarrow$  Bit flips second if + if  
1st bit is 11

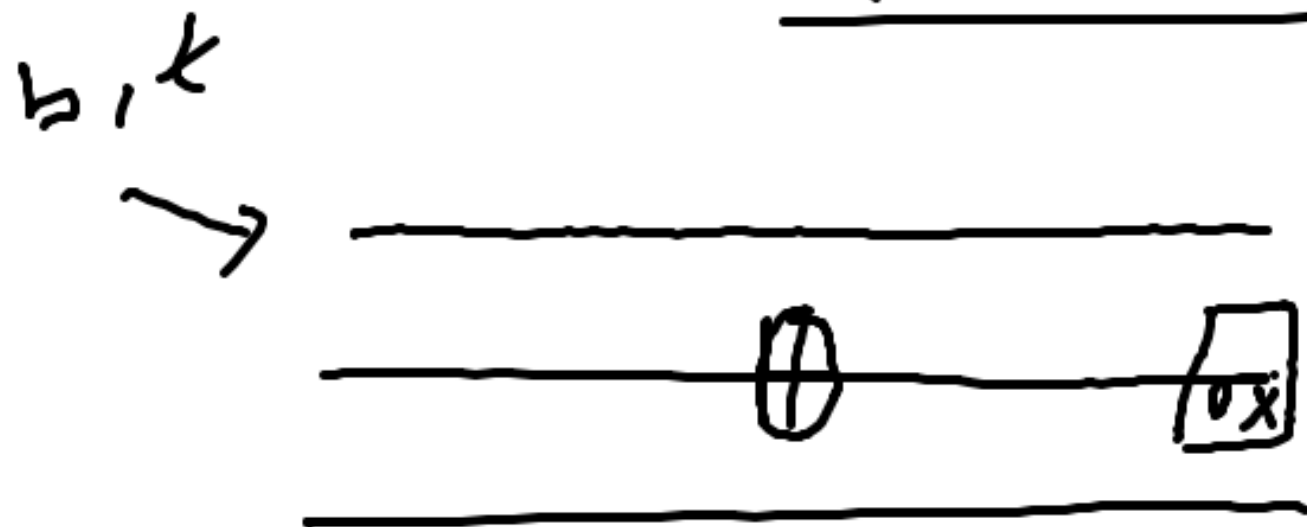
$100 \rangle \Rightarrow 100 \rangle$  C NOT on XOR.  
 $101 \rangle \rightarrow 101 \rangle$  own Inverses.  
 $110 \rangle \rightarrow 111 \rangle$   
 $111 \rangle \rightarrow 110 \rangle$

$$|\psi\rangle|0\rangle = (\alpha|0\rangle + \beta|1\rangle)|0\rangle$$

$$= \alpha \underline{|00\rangle} + \beta \underline{|11\rangle}$$

$|nnnnn\rangle$  - CAT STATES.

# NOTATION



↑  
"time"



$$\sigma_z \sigma_x = i \sigma_y : \begin{pmatrix} 0 & 1 \\ -1 & 0 \end{pmatrix} \begin{pmatrix} 1 & 0 \\ 0 & -1 \end{pmatrix} \begin{pmatrix} \alpha \\ \beta \end{pmatrix} \rightarrow \begin{pmatrix} \alpha \\ -\beta \end{pmatrix}$$

NOT.

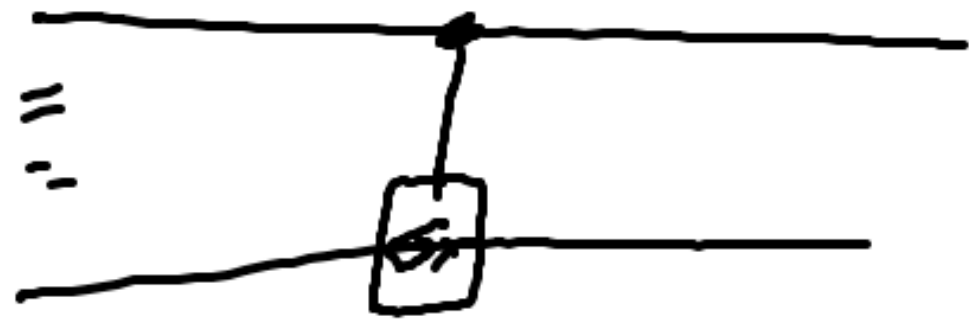
$$\sigma_x = \begin{pmatrix} 0 & 1 \\ 1 & 0 \end{pmatrix}$$

$$\sigma_x \begin{pmatrix} \alpha \\ \beta \end{pmatrix} \rightarrow \begin{pmatrix} \beta \\ \alpha \end{pmatrix}$$

$$\begin{pmatrix} 1 & 0 \\ 0 & -1 \end{pmatrix} \begin{pmatrix} \alpha \\ \beta \end{pmatrix} \rightarrow \begin{pmatrix} \alpha \\ -\beta \end{pmatrix}$$

$$\begin{aligned} |0\rangle &\rightarrow |0\rangle \\ |1\rangle &\rightarrow -|1\rangle \end{aligned}$$

2 bit ops.  $\rightarrow$  control opn.

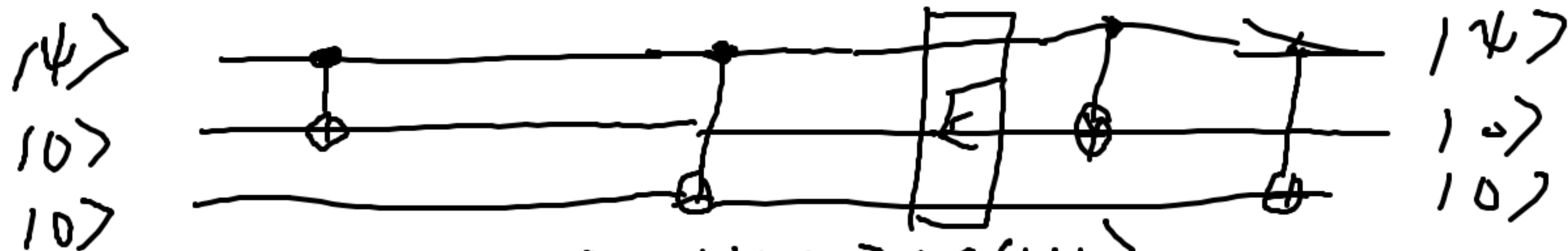


$C\sigma_x, (CX) (CNOT)$   
(XOR)

Hadamard  $\frac{1}{\sqrt{2}} (\sigma_x + \sigma_z) = \frac{1}{\sqrt{2}} \begin{pmatrix} 1 & 1 \\ 1 & -1 \end{pmatrix}$



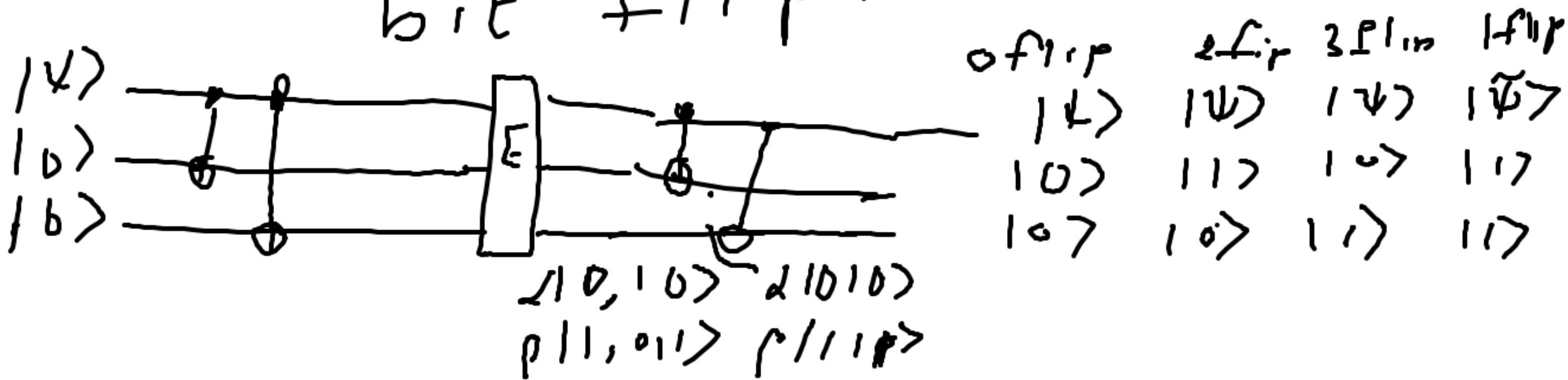
"Copying"

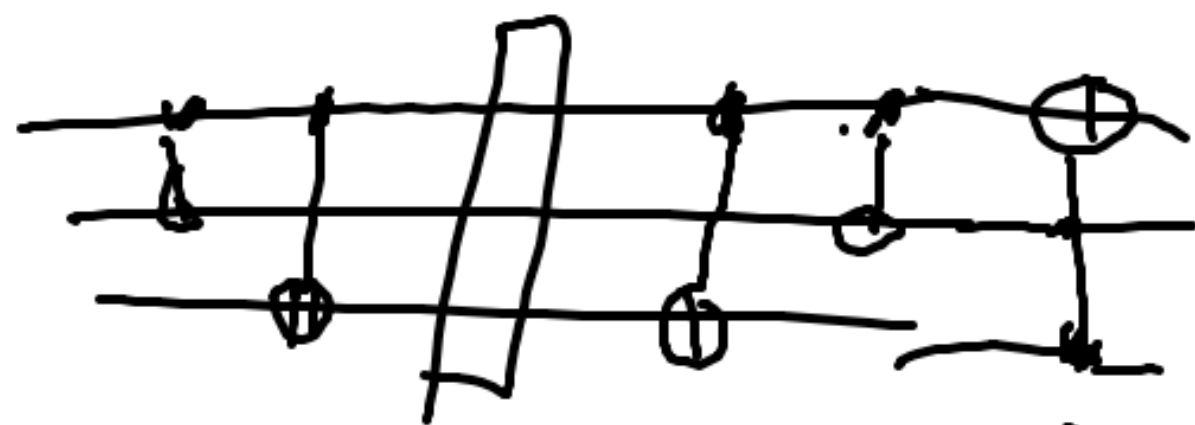


$$\alpha|000\rangle + \beta|110\rangle + \gamma|100\rangle + \delta|111\rangle$$

$E$  is a 1 bit unitary op

bit flip.





$|2\rangle$      $|0\rangle$  ←  
 $|0\rangle$      $|1\rangle$      $|0\rangle$      $|1\rangle$  }  
 $|0\rangle$      $|0\rangle$      $|1\rangle$      $|1\rangle$  }

↑  
Toffoli  
gate.

2 ancilla bits uncorrelated with  
1<sup>st</sup> bit →

$\alpha, \beta$      $\alpha \neq |\phi_1\rangle$      $\rho = |\phi_2\rangle$

States of other bits in  
system.



# Corrects single bit bit flips.

Sign flip.

$$|0\rangle \rightarrow |0\rangle$$

$$|1\rangle \rightarrow -1$$

$$\alpha|0\rangle + \beta|1\rangle$$

$$\rightarrow \alpha|0\rangle - \beta|1\rangle$$

$$|-\rangle = \frac{1}{\sqrt{2}}(|0\rangle - |1\rangle)$$

$$|+\rangle = \frac{1}{\sqrt{2}}(|0\rangle + |1\rangle)$$

$$\text{Sign flip } \frac{1}{\sqrt{2}}(|0\rangle - |1\rangle) \\ \text{Flip } \frac{1}{\sqrt{2}}(|0\rangle + |1\rangle)$$

$$|1\rangle$$

=

$$|-\rangle$$

Sign flip in z basis  $\Rightarrow$  spin flip in x basis



$$\alpha|0\rangle + \beta|1\rangle \Rightarrow \alpha|-\rangle + \beta|+\rangle$$

Hadamard.

$$|0\rangle \rightarrow \frac{1}{\sqrt{2}}(|0\rangle + |1\rangle)$$

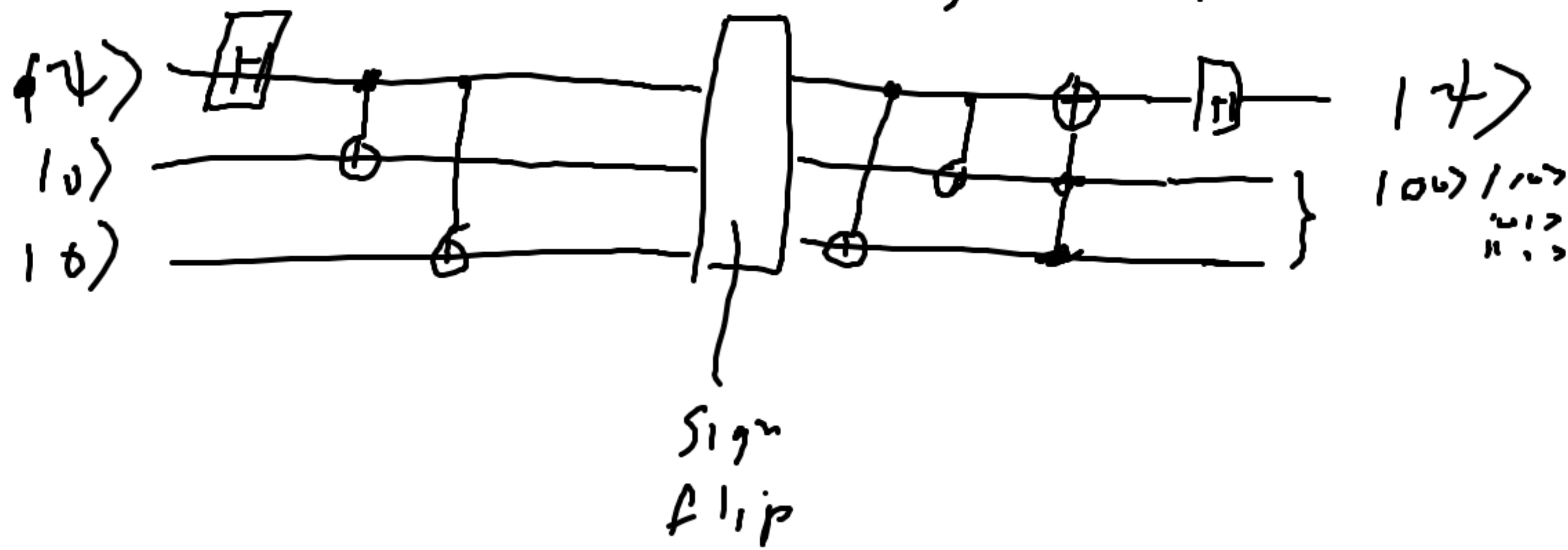
$$|1\rangle \rightarrow \frac{1}{\sqrt{2}}(|0\rangle - |1\rangle)$$

$$\alpha|0\rangle + \beta|1\rangle \rightarrow \alpha|+\rangle + \beta|-\rangle$$

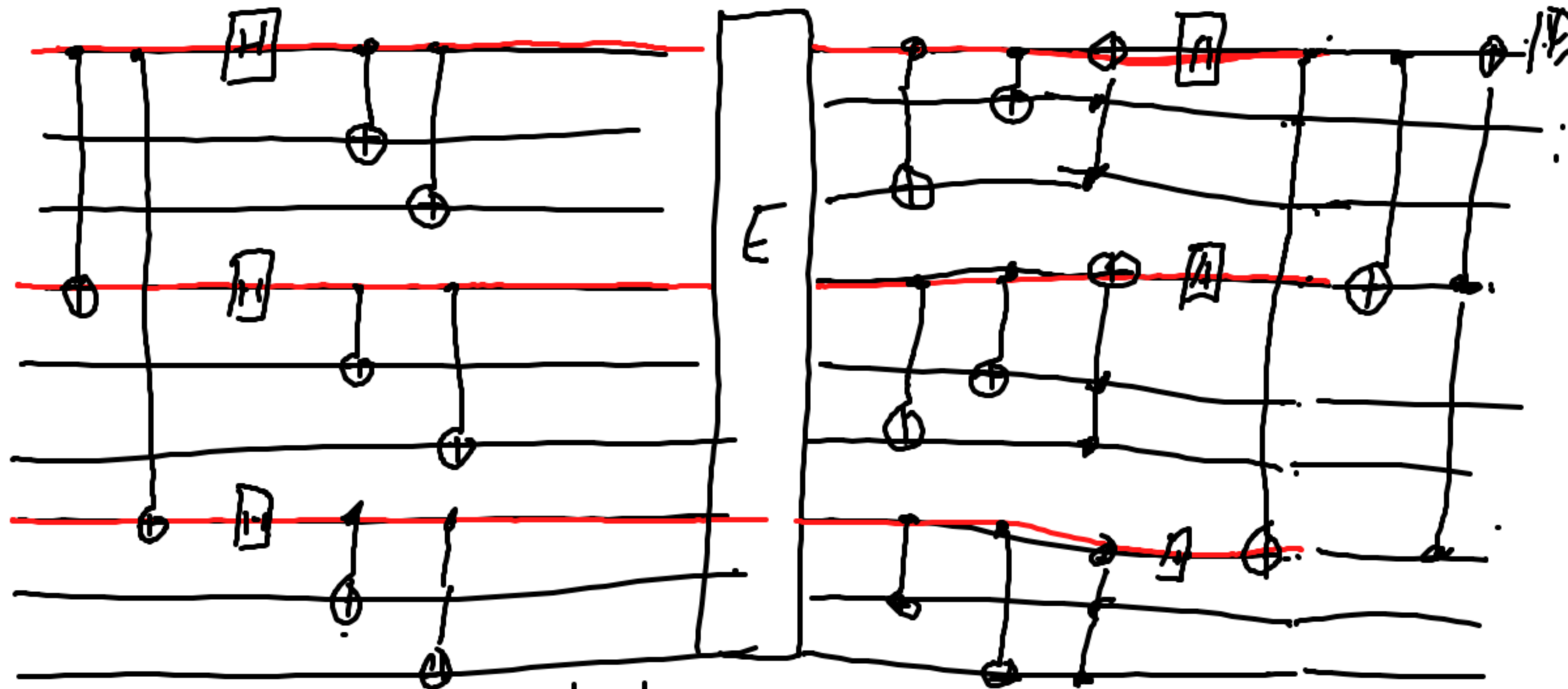
Sign flip in basis.

1

Corrects any single  
Sign flip error.



# Shot Error Corr.



Corrects arbitrary sign + spin  
flips on single bits.  
sign + spin-flip on single bit

This process corrects  
arbitrary unitary 1bit  
errors.

$|1\rangle \rightarrow |0\rangle$  Non Unitary  
error.

$|0\rangle \rightarrow |0\rangle$

Expensive - Need 9 bits (physical)  
for each logical bit.

9 phys. bits.  
~~9 phys. bits.~~

~~~~~

~~~~~

⋮

~~~~~

8 phys  
bits  
per logical  
bit

1996 Laflamme: Showed that  
5 physical bits is smallest  
needed to error correct  
1 bit errors. with example

showed that one could make  
the calc. error tolerant.

•  
④  $|0\rangle \rightarrow .995|1\rangle + .1|0\rangle$   
Again corrected if prob small